# JUL 2 7 2005 CO

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicum. Biju Chandran et al.

Title: CHIP-JOIN PROCESS TO REDUCE ELONGATION MISMATCH BETWEEN THE ADHERENTS AND SEMICONDUCTOR PACKAGE MADE THEREBY

Docket No.: 884.A27US1 Filed: December 21, 2001 Examiner: John B. Vigushin

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 10/023,819

Due Date: July 25, 2005

Group Art Unit: 2827

Confirmation No.: 3133

**Notice of Allowance Date:** 

April 25, 2005

We are transmitting herewith the attached:

X Charge deposit account No. 19-0743 in the amount \$ 70.00 to cover the Large Entity Issue Fee Payment.

- X Charge deposit account No. 19-0743 in the amount \$ 3.00 to cover the Extra Patent Copies Fee (1 copy).
- X Issue Fee Transmittal (Form PTOL-85).
- X Request to Apply Previously Paid Issue Fee (1 Pg.).
- $\underline{X}$  A return postcard.
- <u>X</u> Amendment Under 37 C.F.R. 1.312(a) (11 Pgs.).
- X Comments on statement for reasons of Allowance (2 Pgs.).
- X Communication Concerning Related Applications (1 Pg.).
- $\underline{X}$  Copy of decision on petition to withdraw (1 Pg.).

Please charge any additional required fees for the Issue Fee Payment or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

Ann M. McCrackin

Reg. No. 42,858 AMM1:CMG:zmc

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn – MAIL STOP ISSUE FEE, P.O. Box 1450, Alexandria, VA 22313-1450, on this \_\_\_\_\_\_\_ day of July, 2005.

Zhahabeky M. Carrion

Signature

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S/N 10/023

**PATENT** 

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Biju Chandran et al.

Examiner: John B. Vigushin

Serial No.:

10/023,819

Group Art Unit: 2827

Filed:

December 21, 2001

Docket No.: 884.A27US1

Title

CHIP-JOIN PROCESS TO REDUCE ELONGATION MISMATCH BETWEEN THE ADHERENTS

AND SEMICONDUCTOR PACKAGE MADE THEREBY

### REQUEST TO APPLY PREVIOUSLY PAID ISSUE FEE

Commissioner for Patents

ATTN: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

An issue fee for the above-identified application was previously paid on October 8, 2003. A petition under 37 CFR 1.313(c)(2) to withdraw the above-identified application from issue after payment of the issue fee was previously filed on October 22, 2004. Applicant received a decision, dated March 24, 2005, granting the petition to withdraw.

Applicant respectfully requests that the above-identified previously-paid issue fee be applied towards the fee required by the new Notice of Allowance dated April 25,2005.

The present issue fee has increased from the previously-paid issue fee. Please charge deposit Account No. 19-0743 in the amount of \$70.00 to cover the issue fee increase.

Please charge any additional required fees for the Issued Fee Payment or credit overpayment to Deposit Account No. 19-0743.

This request is being submitted with the new Issue Fee Transmittal Form PTOL-85, along with a copy of the above-identified decision granting the petition to withdraw.

Respectfully submitted,

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6965

By (Inn Ann M. McCrackin

Reg. No. 42,858

AMM1:CMG:zmc

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Attn - MAIL STOP ISSUE FEE, P.O.Box 1450, Alexandria, VA 22313-1450, on 25 day of July, 2005.

Zhanalazky M. Carnon

**PATENT** 10/023,819

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Biju Chandran et al.

Examiner: John Vigushin

Serial No.:

10/023,819

Group Art Unit: 2827

Filed:

December 21, 2001

Docket: 884.A27US1

Title:

CHIP JOIN PROCESS TO REDUCE ELONGATION MISMATCH BETWEEN

THE ADHERENTS AND SEMICONDUCTOR PACKAGE MADE THEREBY

Assignee:

**Intel Corporation** 

Customer No: 21186

## COMMENTS ON STATEMENT FOR REASONS OF ALLOWANCE

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

These comments are in response to the Examiner's statement of reasons for allowance in the Notice of Allowance mailed on April 25, 2005.

On pages 2-3, the Detailed Action implies that the elements of claim 14 are the same as elements of claim 22. Applicants do not concede that the elements of claim 14 are the same as elements of claim 22. For example, claims 22-24 do not include "...in the electronic assembly...".

#### AMENDMENT UNDER 37 C.F.R. § 1.312(a)

Serial Number: 10/023,819 Filing Date: December 21, 2001

Title: COUPLING COEFFICIENT OF THERMAL EXPANSION MISMATCHED MEMBERS

Assignee: Intel Corporation

#### Conclusion

Applicants respectfully submit that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicants' attorney, Lucinda Price (located in Gainesville, Florida), at (352) 373-8804, or Applicants' below-named representative (located in Minneapolis, Minnesota) to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

BIJU CHANDRAN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation P.O. Box 2938

Minneapolis, Minnesota 55402

(612) 349-9592

Ву (

Date July 25, 2005

Ann M. McCrackin

Reg. No. 42,858

Zlasholazky M. Carrion

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**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Biju Chandran et al. oplicant:

0/023.819

Examiner: John Vigushin

Serial No.:

10/023,819

Group Art Unit: 2827

Filed:

December 21, 2001

Docket: 884.A27US1

Title:

CHIP-JOIN PROCESS TO REDUCE ELONGATION MISMATCH BETWEEN

THE ADHERENTS AND SEMICONDUCTOR PACKAGE MADE THEREBY

Assignee:

Intel Corporation

Customer No. 21186

# COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent	Nο
Serial/Faterit	INU.

Filing Date

Attorney Docket

Title

10/023723

December 21,

2001

2004

884.B14US1

SEMICONDUCTOR PACKAGE

WITH PACKAGE-TO-DIE

INTERCONNECT SCHEME (As

Amended)

11/010,123

December 10,

884.B14US2

METHOD OF INTERCONNECTING

DIE AND SUBSTRATE (As

Amended)

Respectfully submitted, BIJU CHANDRAN ET AL. By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6970

Reg. No. 42,858

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#### UNITED STATES PATENT AND TRADEMARK OFFICE

884. AZTUSI

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
P.O. 80X 1450
ALEXANDRIA, VA 22313-1450

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. BOX 2938

MINNEAPOLIS, MN 55402

**COPY MAILED** 

MAR 2 4 2005

OFFICE OF PETITIONS

In re Application of

Biju Chandran, et al.

Application No. 10/023,819

Filed: December 21, 2001

Attorney Docket No. 21940779X00

ON PETITION

This is a decision on the petition, filed October 22, 2004, under 37 CFR 1.313(c)(2) to withdraw the above-identified application from issue after payment of the issue fee.

The petition is GRANTED.

The above-identified application is withdrawn from issue for consideration of a submission under 37 CFR 1.114 (request for continued examination). See 37 CFR 1.313(c)(2).

Petitioner is advised that the issue fee paid on September 7, 2004 in the above-identified application cannot be refunded. If, however, the above-identified application is again allowed, petitioner may request that it be applied towards the issue fee required by the new Notice of Allowance. <sup>1</sup>

Telephone inquiries relating to this decision should be directed to the undersigned at (571) 272-3204.

The application is being referred to Technology Center AU 2841 for further processing of the request for continued examination under 37 CFR 1.114.

Sherry D. Brinkley Petitions Examiner

Office of Petitions

Office of the Deputy Commissioner for Patent Examination Policy

The request to apply the issue fee to the new Notice may be satisfied by completing and returning the new Issue Fee Transmittal Form PTOL-85(b), which includes the following language thereon: "Commissioner for Patents is requested to apply the Issue Fee and Publication Fee (if any) or re-apply any previously paid issue fee to the application identified above." Petitioner is advised that, whether a fee is indicated as being due or not, the Issue Fee Transmittal Form must be completed and timely submitted to avoid abandonment. Note the language in bold text on the first page of the Notice of Allowance and Fee(s) Due (PTOL-85).

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